

Investigations on Grid-Connected DFIWGs Development and Performance Analysis with the Support of Crowbar and STATCOM

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Abstract—These days, one of the most used layouts in the wind power industry is a variable-speed doubly-fed induction wind generator (DFIWG). To provide real and reactive power (P&Q) control during grid failures, this research examines the DFIWG. The system's transient behavior is examined under normal and abnormal conditions. Through rotor side converter (RSC) and grid side converter (GSC) control, Q assistance for the grid, and power converter stress reduction, the suggested control approach achieves system stability while enabling DFIWG to operate smoothly during grid failures. By suppressing rotor and stator overcurrent, DC link voltage (V_{DC}) overshoot, and P&Q oscillations, as well as supporting the grid voltage (GV) under both balanced and unbalanced grid fault scenarios with distinct voltage dips, the suggested technique preserves the system characteristics during grid faults. MATLAB/SIMULINK 2017b is used for time-domain computer simulations. STATCOM and crowbar, two suggested systems, are contrasted. This work proves the effectiveness of the suggested approaches in augmenting the system's fault ride-through (FRT) capacity.

Keywords—Crowbar, Control System, DFIG, STATCOM, Grid Faults

I. INTRODUCTION

DFIWGs have been exposed to grid disruptions when they were directly connected, via lengthy transmission lines (TLs), to the grid at near-to-ground voltages. In grids, imbalanced GVs are the most prevalent. Unequal GV drops on TLs or unbalanced loads connected at the point of common connection (POCC) can cause imbalanced GV [1]-[4]. As a result, key grid codes were distributed by energy system operators, mandating that DFIWGs be able to sustain a 2% steady-state and 4% short-term imbalance in GV without triggering [5]-[8]. Negative sequence stator/rotor current parts are produced by an imbalanced GV. There will be strong fluctuations in V_{DC} , electromagnetic torque, and power at twofold supply frequency. Consequently, an imbalanced generator voltage can damage generator bearings, V_{DC} at capacitors, and DC/AC inverters. To solve these issues with no disconnecting the generating systems, the DFIWG controller systems must handle these conditions [9]-[12]. GSCs are controlled to regulate V_{DC} and total Q

injected into the grid, while RSCs are managed to adjust stator P&Q power [13].

Various control systems, such as vector control (VC) and direct power control (DPC), are investigated to regulate DFIWG in a typical setting [14]-[16]. One of the favored control strategies was the DPC of DFIWG. When weighed against VC, DPC offers some benefits, including ease and quick response time [17], [18]. Under uneven and deformed GV, the functioning of DPC is investigated [19]-[21].

RSC-based DPC was controlled by PI resonant controllers (PIRs) in [22]. To accomplish various control objectives, a power adjustment method (PAM) is included. To obtain the (+ and - sequence) parts of GV and current, nevertheless the stator deconstruction method is necessary. A static target framework DPC-based sliding mode (SM) regulator with a PAM was developed in [23]. A vector-PI (VPI) controller was developed in [24] to enhance the DPC of DFIWG in the presence of harmonic GV. Synchronous relative frame implementation was used for the regulator. DPC and a separated framework for DFIWG that utilizes lagging control are coupled in [19]. Tests are conducted on the suggested controller's functionality in both symmetric voltage dip and typical circumstances. Rather than employing traditional immediate power elements, the DPC method is constructed in [25] utilizing expanded concepts of power. A second-order (SO) generalized integrator (GI) is used to estimate the delayed values and filtered stator voltage. DPC employs frequency adaptive vector PI (FAVPI) in [26] to lessen the impact of harmoniously and imbalanced GV. DAVPI requires an axis conversion and a frequency estimation. A voltage-modulating DPC (VM-DPC) was proposed in [27] to enhance both intermittent and constant-state performance in balance GV scenarios. An additional parallel adjuster is intended to work in tandem with the VM-DPC to regulate (-sequence) current and enhance stator current, active power (P), and reactive power (Q). Nonetheless, the gathering of the voltage and current elements of a stator in (+ and - sequence) is necessary. Ref. [28] uses extended P to build an SM-DPC approach for DFIWG.

In [29], the MP-DPC of RSC under unbalanced GV is examined. Moreover, observations of the rotor current (RC) were necessary for the computation of the rotor voltage (RV) vector. A unified PAM is suggested for a low complexity MP-DPC that was designed in [30], [31]. MP-based RC control for matrix converters was designed in [22]. RV vectors were delivered in [23] during a predetermined sample interval. On the other hand, there might be a rise in shift frequency and losses.

PI regulators (PIR) have been made to function well both in abrupt and stable conditions when it comes to electrical control. Nonetheless, there are two grid frequency (GF) fluctuation elements when the grid is out of sync. Because of the inadequate gain at two times GF, PIR would not be suitable [22]. To address the shortcomings of PIR, PIR was proposed [32]. PIR has the potential to achieve zero steady-state error; however, the pole distribution of the control object DFIWG may cause an unanticipated peak in magnitude response at a GF greater than the resonant frequency, which is harmful to stable closed-loop (CL) operation [33], [34]. Precise AC pulse monitoring and sufficient CL phase tolerance are features of the PIR. PIR can be built to eliminate the DFIWG stator P and Q pulsation components based on pole-zero cancellation to prevent the surprise gain spike [33].

The performance study of DFIWG to the weak grid is examined in this research. To prevent the axial transformation of stator voltage and current, control of the RSC is built on a stationary reference frame. Equations for electromagnetic torque and stator power are derived using rotor flux under balance and imbalanced GV as inputs. To assess the DFIWG's performance, it is used with a crowbar, a FACTS device, and no additional tools. Simulation studies are conducted to assess DFIWG's performance. The performance of several approaches is assessed under balanced and unbalanced GV.

II. DESCRIPTION AND MATHEMATICAL MODELING OF THE INVESTIGATED SYSTEM

A. System Description

A two-level IGBT voltage source converter (VSC) system in a back-to-back converter (BTBC) configuration is normally used for DFIWG. Since both stator and rotor can feed energy to the grid, the generator is known as DFIWG. For grid-connected DFIWG, the stator is directly connected to the grid, and the rotor is connected to the grid through BTBC. The power converter of the rotor consists of two components, the RSC and GSC, which are controlled independently [35]-[38].

RSC is used to provide the required magnetizing current in the rotor winding for controlling P and Q which flow from the stator terminals of DFIWG to the grid. The main task of the GSC is to keep the DCLV constant. This system allows a variable speed operation over a range of (+/-) 30% around the synchronous speed. Therefore, the power flow in the rotor circuit is bidirectional: it can flow from the grid to the rotor or vice versa. This requires a four-quadrant converter system as shown in Fig. 1. However, the converter system needs to process only around 30% of the rated power. The use of reduced-capacity converters results in a reduction in cost, weight, and physical size as well [39]-[41].

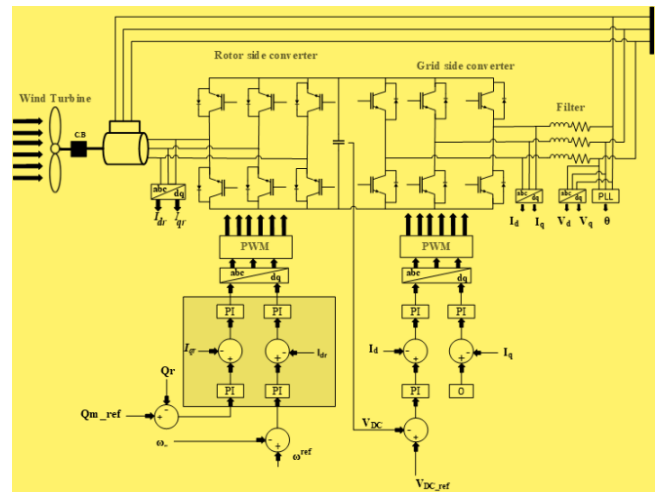


Fig. 1. Grid-tied DFIWG

B. Mathematical Model

A mathematical model of DFIWG, derived in an appropriate dq frame reference is established. The grid voltage-oriented VC is used for GSC to maintain a constant DCLV and to compensate for Q at the network. The stator field-oriented VC is adopted in the RSC control strategy, providing efficient handling of P and Q at the stator [40], [42]. The winding arrangement of the asymmetrical induction machine (IM) is shown in Fig. 2 [43], [44].

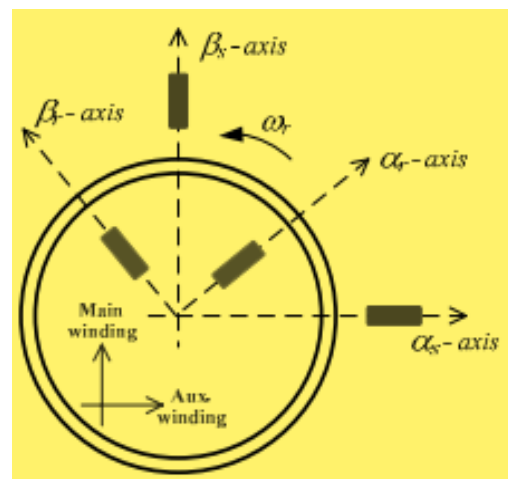


Fig. 2. Configuration of the winding for IMs

The equivalent circuit diagram of an IM is shown in Fig. 3 and Fig. 4. In these Figs, the IM is represented as a two-phase machines. For the modeling of DFIWG in a synchronously rotating frame reference, we need to represent the two-phase stator (ds-qs) and rotor (dr-qr) circuit variables in a synchronously rotating (d-q) frame [45], [46].

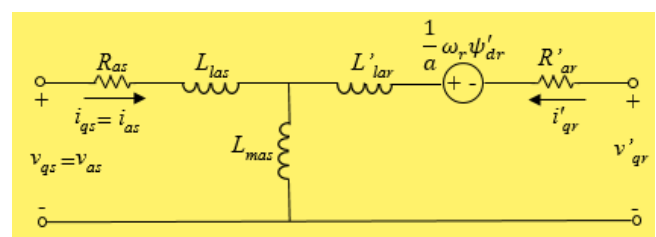


Fig. 3. Dynamic d-q equivalent circuit of DFIG (q-axis circuit)

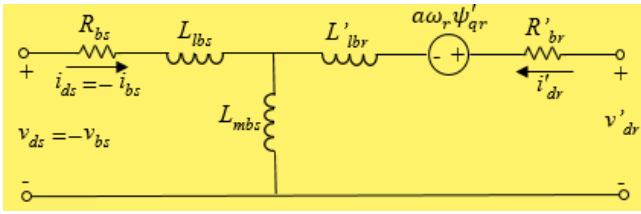


Fig. 4. Dynamic d-q equivalent circuit of DFIG (d-axis circuit)

The stator circuit equations are represented by equations (1) and (2), and rotor circuit equations are represented by equations (3) and (4) given:

$$V_{ds} = R_S I_{ds} - \frac{d\psi_{ds}}{dt} - \omega_S \psi_{qs} \quad (1)$$

$$V_{qs} = R_S I_{qs} + \frac{d\psi_{qs}}{dt} + \omega_S \psi_{ds} \quad (2)$$

$$V_{dr} = R_r I_{dr} + \frac{d\psi_{dr}}{dt} - (\omega_S - \omega_r) \psi_{qr} \quad (3)$$

$$V_{qr} = R_r I_{qr} + \frac{d\psi_{qr}}{dt} + (\omega_S - \omega_r) \psi_{dr} \quad (4)$$

$$\psi_{ds} = L_S I_{ds} + L_m I_{dr} \quad (5)$$

$$\psi_{qs} = L_S I_{qs} + L_m I_{qr} \quad (6)$$

$$\psi_{dr} = L_r I_{dr} + L_m I_{ds} \quad (7)$$

$$\psi_{qr} = L_r I_{qr} + L_m I_{qs} \quad (8)$$

$$T_e = \frac{3}{2} P (\psi_{ds} I_{qs} - \psi_{qs} I_{ds}) \quad (9)$$

Where all the variables are in a synchronously rotating frame. The bracketed terms are defined as the back e.m.f. or counter e.m.f due to the rotation of axes as in the case of DC machines. When the angular speed $\omega_r = 0$ the speed e.m.f due to the d and q axis is zero and the equations change to the stationary form.

Owing to the rotor circuit, if the rotor is blocked or not moving, i.e. $\omega_r = 0$, the machine equations can be written similarly to stator equations as in 3 and 4. All the parameters are referred to the primary circuit, which is a stator in this case. Let the rotor rotate at an ω_r , then the d-q axes fixed on the rotor fictitiously will move at a relative speed $\omega_S - \omega_r$. The flux linkage expressions in terms of current can be written from Fig. 3 and Fig. 4 as in (5)-(8).

Equation (1) to equation (8) describes the complete electrical modeling of DFIG. Whereas Eq. (9) expresses the relations of mechanical parameters which are an essential part of the modeling.

C. Control Of DFIG

Fig. 5 shows the block diagram of DFIG with stator voltage-oriented control (SVOC). The abc/dq and dq/abc transformation blocks transform the variables in the abc stationary reference frame to the dq synchronous reference frame, and vice versa. The angle of the SVOC can be obtained via the measured 3-phase stator voltages [47], [48].

The MPPT block generates the reference torque based on the optimal torque method. The reference for the d-axis rotor

current is calculated according to equation (3). For a given stator Q_s^* , the q-axis rotor current reference is calculated by equation (4). The reference dq-axis currents are then compared to the measured values, and the errors are passed through PI controllers. The output of the PI controllers is the dq-axis rotor voltage references in the synchronous frame, which are transformed into a three-phase reference for rotor voltages in the stationary frame. The rotor reference voltages can serve as the three-phase modulating waveforms in carrier-based modulation schemes or be converted into a reference space vector for the space vector modulation (SVM). PWM block generates gating signals for the RSC. The GSC performs two main functions: (1) it keeps the V_{DC} constant, and (2) provides Q to the grid when required. The Q reference can be set to zero for the unity power factor (UPF) operation of the converter. The overall PF of the DFIG is then controlled by the RSC [49]-[52].

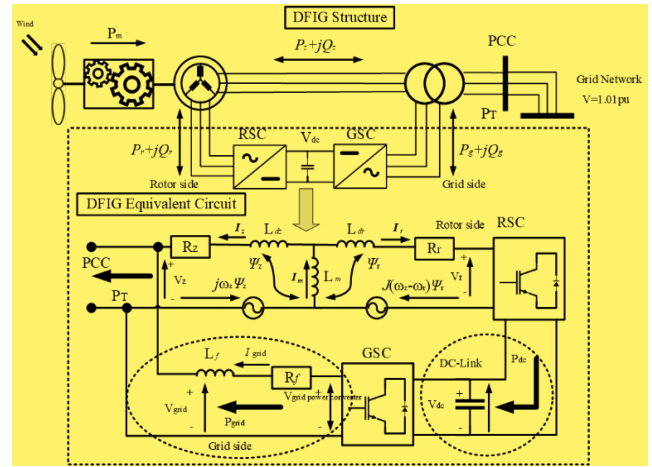


Fig. 5. Block diagram of a DFIG with stator voltage-oriented control

III. SIMULATION RESULTS AND DISCUSSION

Simulation has been done on a 1.5 MW, 575V, 50 Hz, 3 poles DFIG by using MATLAB/Simulink [10]. Studying DFIG performance includes time response of P, Q, and V_{DC} during normal and abnormal conditions. Active crowbar (AC) and STATCOM devices are used for enhancement of their performance, and their connections are illustrated in Fig. 6. The investigated configurations show the role of these suggested tools. In the obtained results, C1, C2, and C3 represent without protection, with STATCOM, and with AC, respectively.

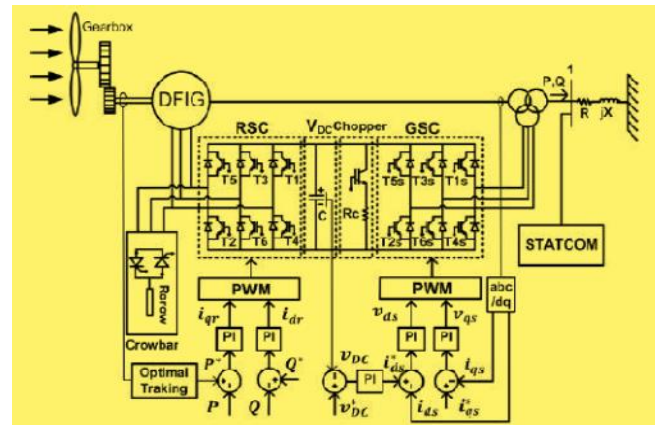


Fig. 6. Schematic diagram of AC and STATCOM with DFIG

A. Studying the Performance of DFIG Under Starting

Fig. 7 from (a) to (c) shows the performance of grid-connected DFIG in the case of balanced grid operation. Set point $P=1.5$ MW and $Q=0$ which means that DFIG is operated at unity power factor. The V_{DC} is maintained constant at 1150 V. Results show that both STATCOM and AC enhanced the dynamic performance of DFIG as shown in Fig. 7.

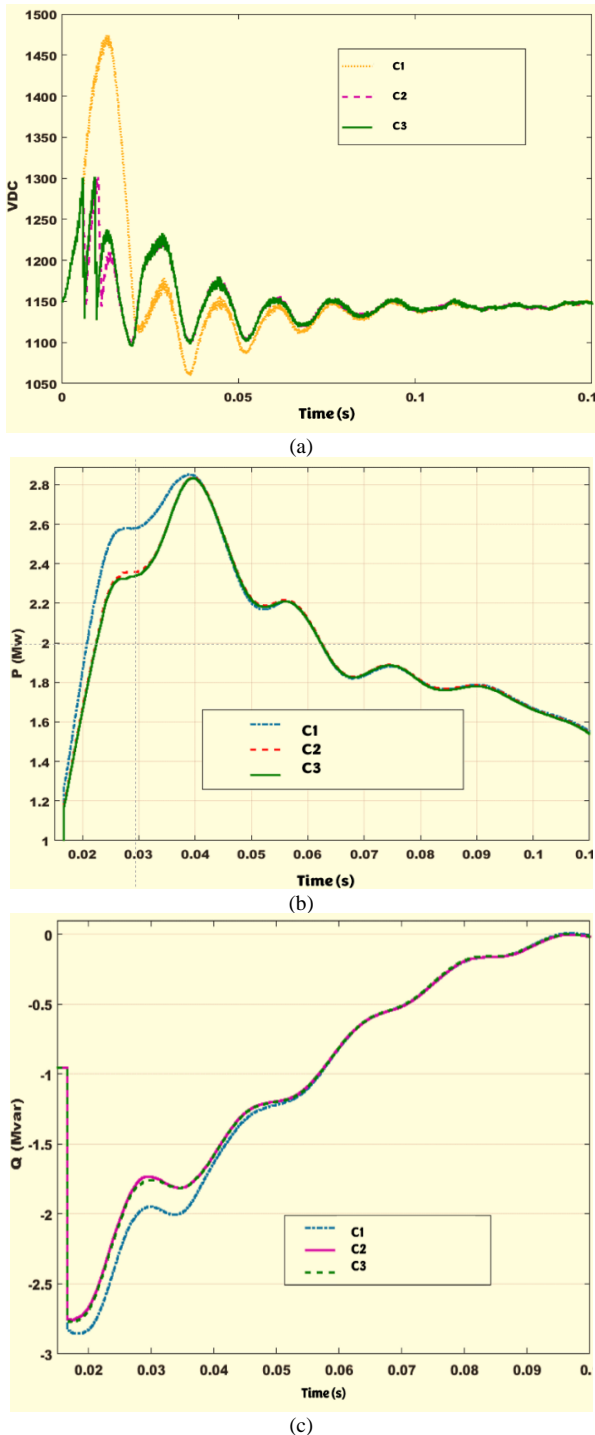


Fig. 7. Simulation results (a) V_{DC} , (b) P, (c) Q

B. Studying The Dynamic Performance of DFIG Under Three Phases to Ground Fault

Dynamic performance of the system is studied AC and STATCOM. This fault leads to the voltage decreasing to zero

roughly. In this paper assumption of GV disturbance occurs at $t=0.25$ s and cleared at 0.3 s. During the fault period, the P decreased to zero, and after fault clearing P returned to its rated value of 1.5 MW as seen in Fig. 8 (b), but Q drawn from the grid increased during the fault period which reached 1 Mvar roughly and after fault clearing Q returns to its rated value 0 as seen in Fig. 8 (c). The V_{DC} increased during the fault period to reach 1800 V, but AC and STATCOM decreased to reach 1300 V which protects the back-to-back converter from damage as seen in Fig. 8 (a). Fig. 9 (a-c) shows STATCOM has a superior performance compared to AC and without protection. DFIG is still connected to the grid and Q supply is found to mean FRT capability realization.

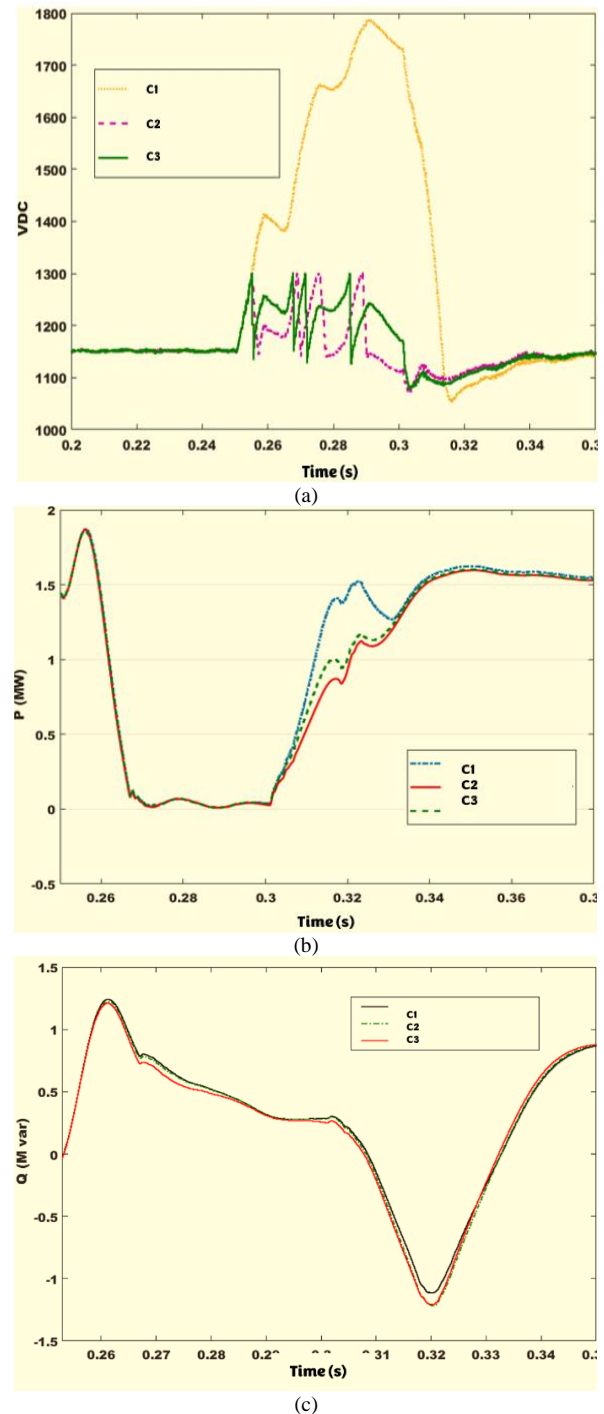


Fig. 8. Simulation results (a) V_{DC} , (b) P, (c) Q

C. Studying the Dynamic Performance of DFIG Under Double Line to Ground Fault

At the occurrence of this fault, the P drops to nearly 0.5 MW with AC and reaches 0.62 MW with STATCOM as shown in Fig. 9 (b). The V_{DC} reaches about 1330 V without protection and reaches 1250 V with STATCOM but with AC reaches 1200 V only as seen in Fig. 9 (a). The DFIG absorbs about 1 Mvar during the fault period but STATCOM gives better performance for P&Q during the fault period compared to AC as depicted in Fig. 9 (c). Although AC gives better performance for the V_{DC} .

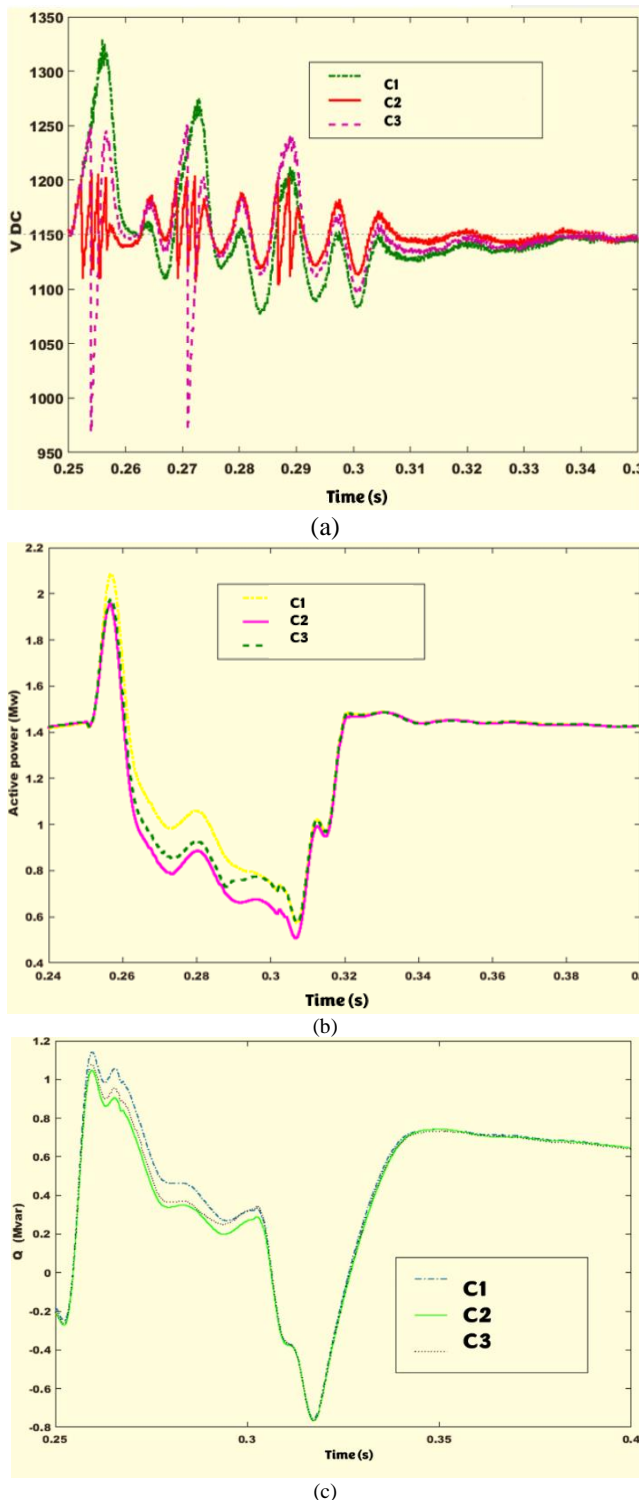


Fig. 9. Simulation results (a) V_{DC}, (b) P, (c) Q

IV. CONCLUSION

When failures occur, DFIG is under a lot of stress. By using an AC and STATCOM, DFIG may remain connected to the grid and restrict currents and voltages below predetermined thresholds. The Q regulation cannot be provided by DFIG equipped with AC, although it can withstand grid disturbances, according to simulations. Investigations are conducted into the use of a STATCOM linked to a DFIG to enable uninterrupted FRT of GV faults. Whereas the DFIG can carry on with its nominal operation and satisfy any grid code requirement without requiring additional protective mechanisms, the STATCOM can make up for the defective line voltage. Simulation results declared that the dip in GV leads to overvoltage in the DC link and a decrease in P. From the previous discussion, we find that the DFIG connected to the grid gives a better dynamic performance with the STATCOM tool in case of three-phase fault and double line to ground fault compared to AC. AC and STATCOM tools have succeeded in enhancing the machine's performance.

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